Magnetoelectric Flexural Gate Transistor with NanoTesla Sensitivity

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Abstract— Magnetic sensors capable of detecting tiny ac magnetic fields ranging from micro-Tesla to picoTesla are of great interest. In this paper, we demonstrate an integrated magnetoelectric flexural gate transistor (MEFGT) with nanoTesla magnetic field detection sensitivity at room temperature. The device capacitively couples a Metglas® (Fe0.85B0.05Si0.1) based magnetostrictive unimorph micromechanical cantilever beam to the gate of an n-channel field effect transistor. Using this sensor configuration, a sensitivity of 0.23 mV/μT and a minimum detectable field of 60 nT/√Hz at 1 Hz and 1.5 mV/μT and 150 pT/√Hz at the flexural resonance of the cantilever structure of 4.9 kHz were obtained. The results demonstrate a significant improvement in the thin film ME sensor integration with standard CMOS process and open the possibility of monolithic magnetic sensor arrays fabrication for biomedical imaging applications.

Index Terms— Magnetoelectric (ME), magnetostrixtion, Metglas® (Fe0.85B0.05Si0.1) thin films, microelectromechanical systems (MEMS), magnetoelectric flexural gate transistor (MEFGT).

I. INTRODUCTION

A CCURATE measurements of tiny magnetic fields are of great interest in different applications. Medium accuracy sensors such as anisotropic magnetoresistive (AMR) and giant magnetoresistive (GMR) sensors have a resolution around 10 nT. While fluxgate sensors have been demonstrated to measure DC and low frequency ac fields up to 1 mT with a resolution of 10 pT, they typically have a large footprint with centimeter sized coil and are not amenable to miniaturization into array format [1-3]. Till date, measurement of magnetic fields below picoTesla signal can only be performed with the most sensitive magnetic sensors – superconducting quantum interference device (SQUID) [4], which are typically used in high precision scientific experiments and in biomedical imaging applications. In recent years, laboratory studies on magnetoelectric (ME) effect [5-6] sensors have sparked interest in the quest towards achieving low cost, room temperature and ultra-sensitive magnetometers. Such bulk sensors constructed from ME laminates were reported to exhibit large ME coupling coefficient of 21.46 V/cm•Oe [7] and a resolution of $2\times10^{-11}$ T/√Hz at resonance [8].

In consideration of the need for high spatial resolution of magnetic field sensing applications, the current challenges in the state of the art pertain to the realization of high sensitivity magnetic sensor arrays. Motivated by this challenge, in this paper, we introduce a new type of chip-scale magnetic sensors – a magnetoelectric flexural gate transistor (MEFGT) sensor by integrating a magnetostrictive (MS) Metglas® (Fe0.85B0.05Si0.1) thin film micromechanical cantilever directly atop a sensing and amplifying transistor, as shown in Figure 1(a). The device combines the merits of magnetoelectric laminates [9] with the concepts of a flexural-gate transistor [10]. The direct integration of the Metglas® films on the MOS circuit provides the advantages of high reliability, small size,
II. PRINCIPLE OF OPERATION

The MEFGT device consists of a freestanding unimorph cantilever made of magnetostrictive Metglas® film on a passive titanium film and is located directly atop an extended gate of a MOS readout transistor and with a small air gap in between [9]. Time varying ac magnetic field induces flexural bending motion in the suspended cantilever via magnetostriction effect which, in turn, modulates the air-gap capacitance. The air-gap capacitance modulation is sensed directly via modulation of the channel charge density in the field effect transistors (FETs) and is amplified by the transconductance of the FETs in the form of drain current modulation (Fig 1(b)). Therefore, the MEFGT combines the benefits of high-deflection property of MS cantilever sensors with FET based motion sensing and amplification.

III. FABRICATION OF MEFGT

The major fabrication process steps shown in Figure 2 include the following: 1) standard NMOS transistor fabrication processes (Figure 2(a) – (d)); 2) deposition of the sacrificial layer; 3) deposition and patterning of mechanically active structure (Figure 2(e)); and 4) final anneal and release (Figure 2(f)). A total of six photolithographic steps were used in the fabrication of the MEFGT.

The fabrication process began with the growth of a 1 µm thick field oxide (SiO₂) [thermal oxidation at 1050°C at for 3 hours] on a single-side-polished, 10¹⁵ cm⁻³ doped, (100) p-type silicon wafer. This is followed by a standard NMOS fabrication process that includes formation of n⁺ doped source drain structures via solid source diffusion [20 min POCl₃ pre-deposition at 1000°C and 30 min, and 1050°C wet oxidation drive-in], gate oxide growth [950°C 30 min dry oxidation and 30 min post anneal] and gate electrode metallization [10 nm/100 nm thick Ti/Pt metal deposition [via RF sputtering at 200 W power under 5 mTorr pressure]. For the fabrication of the transistors, four photolithography steps were used for source/drain window patterning, gate oxide deposition, contact...
via etching, and electrode patterning (Figure 2 (b) - (d)). This is followed by the fabrication of the top flexural gate structure, for which a 500 nm amorphous silicon was chosen as the sacrificial material, deposited by plasma enhanced chemical vapor deposition [PECVD using Ar and SiH₄ precursors, RF power of 200 W at 2 Torr, 220 °C)]. Next, a 500 nm thick layer of titanium was deposited followed by ion beam sputtering of 100 nm thick Metglas® film, and were patterned using a lift-off technique to form a 300 µm × 100 µm MS cantilever, as shown in Figure 2 (e). A DC magnetic field of 600 Oe was applied to polarize the Metglas® film domains during the deposition process [14, 15]. Finally, the cantilever structures were released by a xenon difluoride (XeF₂) vapor phase etching process and annealed at 350 °C (to release internal stress) (Figure 2 (f)). The use of an amorphous silicon sacrificial layer ensures a rapid release of the cantilever structure while the use of the XeF₂ vapor phase etching minimizes any damage of the transistor gate oxide and provides high selectivity to photoresist protection mask used to prevent the lateral undercutting of the anchor areas. Figure 3(a) and (b) show the SEM image of MEFGT and the freestanding cantilever gate structure, respectively. The inset of Figure 3 (b) shows that the top flexural gate is bent-up by ~5 µm at the tip end due to a larger residual tensile stress in the Metglas® film in comparison to the Ti passive layer leading to a decrease in the air gap capacitance.

IV. MEFGT CHARACTERIZATION

A. Film Characterization

The magnetic properties of the deposited Metglas® thin film were characterized in magnetic properties measurement system [Quantum Design Inc. MPMS System] [16]. The films show a low coercivity of ~6 Oe and high magnetization ~2 Tesla, as shown in Figure 4(a). Magnetostriuctive coefficient of the films was characterized by using a laser vibrometer setup. The entire sample was placed in a custom made Helmholtz coil and the deflection of the cantilever tip was accurately measured as a function of the applied magnetic field. The applied magnetic field using the Helmholtz coil was accurately calibrated using a LakeShore 450 Gaussmeter. Figure 4 (b) shows the measured magnetostrictive coefficient as a function of the applied in-plane magnetic field. The maximum magnetostriective coefficient in the perpendicular direction (i.e. applied magnetic field is orthogonal to the magnetization (poling) direction of the Metglas® film) is ~7 times smaller than in the parallel direction and therefore provides a clear directional (vector) sensitivity to the applied magnetic field.

The performance of the top flexural gate capacitor as a function of applied magnetic bias field was measured using an Andeen Hagerling Inc. AH 2700A ultra-precision capacitance...
bridge at 100 Hz. The air-gap capacitance as a function of the applied magnetic field is shown in Figure 5. The derivative of this curve defined as the differential capacitance between the top flexural gate and the extended transistor gate pad as a function of the applied magnetic bias field is also shown in Figure 5. As expected, the capacitance changes as a function of the applied magnetic field due to the magnetostrictive effect in Metglas®. The asymmetric differential response can be explained by the slight asymmetry in the coercive fields seen in the B-H loop (Fig. 4(a)) in the sputtered films. The differential capacitance peaks occur at 5 Oe & -7 Oe which are different than the bias magnetic field of 2.5 Oe at which the peak values in magnetostriction coefficient is obtained in Figure 4(b). This is due to the fact that the two different MS cantilevers were used in these measurements and the two cantilever films had undergone different deposition and fabrication steps and have slightly different aspect ratios which can alter the magnetization profiles in the films.

Fig. 7. (a) Low frequency output voltage as a function of the DC magnetic field. The data was measured at 20 Hz and $H_b = 0.38$ Oe. (b) MEGFT frequency response measurement and simulation in both air and vacuum. (c) Output noise performance of MEGFT. (d) Frequency spectrum of 3 nanoTesla input ac magnetic field in air. (The DC magnetic bias field is set at 7 Oe. 3nT ac magnetic field is the minimum generated by the measurement setup.)
 Device Characterization

The devices were placed in a dual-in-line package and electrical connections were made via conventional wire bonding. The performance of the MEFGT was thereafter characterized using the configuration illustrated in Figure 6. These measurements were performed in a magnetically shielded iron box to prevent unwanted interference from earth’s magnetic field and other stray electromagnetic noise. An electromagnet driven by a function generator which included an appropriately set current, provided both input ac magnetic field, \( H_{\text{in}} \) (signal) and the bias magnetic field, \( H_{\text{DC}} \) (for maximum sensitivity). The output transistor voltage was measured using a Stanford Research SRS-830 lock-in amplifier and a 35670A HP dynamic signal analyzer (DSA).

Figure 7 (a) shows the low frequency (20 Hz) output voltage of the transistor integrated with the MS cantilever as a function of the applied DC magnetic bias field as measured by the common source readout amplifier circuit for a 0.38 Oe input ac magnetic field. The result shows that the output voltage follows the input capacitance change with DC magnetic bias field.

The resonance response of the MEFGT was studied both in air and vacuum (10^{-3} Torr), as shown in Figure 7(b). The vacuum measurement was performed in a separate setup. A custom-made Helmholtz coil was used to generate the ac input magnetic field while the DC bias field was provided by a permanent magnet placed inside the chamber. The use of the permanent magnet avoids the coil from overheating due to the passage of large dc current. The magnetic field of the magnet was also calibrated by the Gaussmeter and the position of the sensor was adjusted to match the bias field corresponding to maximum output. The entire set-up was pumped down to mTorr pressure range using a dry pump. After the vacuum (pressure) in the chamber stabilized, the chamber was valved off and physically disconnected from the pumping system to isolate it from the undesired mechanical vibration of the pump. The output of the sensor was measured by sweeping the frequency of the magnetic field at constant amplitude. The device resonance frequencies and the \( Q \)-factor were determined to be 4.27 kHz and 12 (in air) and 4.9 kHz and 24 (in vacuum) respectively. At resonance, the output voltage increases by ~10 times (in air) and ~20 times (in vacuum) in comparison to the off resonance output. The observed resonance behavior in both air and vacuum was confirmed using a modal analysis in COMSOL® finite element multiphysics software [17]. By adjusting the mass damping parameter in the Rayleigh damping model, we were able to match the frequency response in air.

The noise spectrum of MEFGT is shown in Figure 6 (c). The noise floor of the input transistor limits the noise floor of the entire system. From Figs 7 (a), (b), & (c), we obtain a sensitivity of 0.23 mV/μT @ 1 Hz corresponding to a minimum detectable field of 60 nT/√Hz @ 1 Hz, and a sensitivity of 1.5 mV/μT at the resonance frequency of 4.27 kHz corresponding to a minimum detectable field of 150 pT/√Hz. In Figure 7(d), we demonstrate the detection of 3 nano Tesla input magnetic signal at the resonant frequency. This result represents a significant enhancement in comparison to the first integrated ME sensor demonstrated by Y. Lu and A. Nathan with 40 micro Tesla detectable field [18].

V. MODELING

To better understand the MEFGT performance, we will investigate the two main factors that limit the performance of the device in this following section.

The input charge signal originating from the flexural gate displacement can be expressed as

\[
q_{\text{in}} = \Delta C \cdot V_{\text{bias}}
\]

where \( q_{\text{in}} \) is the input gate charge, \( \Delta C \) is the differential capacitance and \( V_{\text{bias}} \) is the potential difference across the flexural gate and transistor gate. Due to the stress mismatch in the constituent layers, the parallel plate assumption for \( C_0 \) is no longer valid. In order to better understand the effect of the non-ideal air gap of the fabricated devices, a more accurate formulation of the capacitance is required and is modeled next.

A. Capacitance Modeling

The analytical model of the static characteristics of the microcantilever expresses the relation between the applied magnetic field and the differential capacitance. The parallel plate capacitance between the freestanding MS cantilever and the gate electrode was modeled by accounting for the anticlastic bending of the cantilever. A schematic illustration of the cantilever is shown in Figure 8(a). Applying a two-
dimensional (2D) bowing approximation, which results from the 2D residual stress, we extract two parameters, effective area, \( A_{\text{eff}} \) and effective gap, \( d_{\text{eff}} \). The effective shape along the width direction using a polynomial curve fitting from the white light interference microscope image can be approximated as

\[
w_{\text{eff}}(d) = w \left( 1 - 0.2 \frac{(d - 0.5)^2}{w} \right)
\]

where \( w \) is the cantilever width and \( d \) is the distance between cantilever (top flexural gate) and bottom plate (transistor gate pad). Correcting for the fringing field effect, cantilever release via holes, and the cantilever length, the effective area \( A_{\text{eff}} \) is expressed as

\[
A_{\text{eff}} = \alpha(A) \int_{d} w \left( 1 - 0.2 \frac{2(d - 0.5)}{w} \right) \Delta d
\]

where \( \varepsilon_0 \) is air permittivity and \( \alpha(A) \) is the correction factor. In a similar method from the empirical bending profile of the cantilever, an approximation for the effective gap \( d_{\text{eff}} \) can be obtained as follows:

\[
d_{\text{eff}}(x, y) = 0.5 + \frac{y^2}{18000} + \frac{x^2}{5000}
\]

and the differential capacitance is given by

\[
\Delta C = C_{\text{eff}} \frac{\delta}{d_{\text{eff}}}
\]

where \( \delta \) is the top flexural gate deflection, which can be calculated as\[19, 20\]

\[
\delta = \frac{3l^2}{t_m A B^4} \frac{AB(B+1)}{2A(2B+3B^2+2B^3)+1} d_{\text{eff}}^m H_m
\]

where \( A = E_p/E_m \) is Young’s modulus ratio, \( B = t_p/t_m \) is thickness ratio, \( d_{\text{eff}}^m \) is magnetostrictive coefficient, and \( H_m \) is the input ac magnetic field.

Table 1 summarizes all the parameter values used in the modeling of the flexural gate differential capacitance. 3D COMSOL simulations with anticlastic bending (Figure 8(b)) were also performed to compare the results with the experimental and analytical data. The results in Figure 9 show that the model agrees well with the measurements. The overestimation in the modeling result arises due to the aspect ratio difference \[7\], unaccounted fringing fields of the cantilever etching vias, and the differences between the numerical values of the various parameters and those of the

<table>
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<th>Symbol</th>
<th>Description</th>
<th>Value</th>
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<tr>
<td>( E_p )</td>
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</tr>
<tr>
<td>( E_m )</td>
<td>Young’s modulus for Metglas, (GPa)</td>
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<td>Density for Ti, (g/cm³)</td>
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<tr>
<td>( \rho_m )</td>
<td>Density for Metglas, (g/cm³)</td>
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</tr>
<tr>
<td>( w )</td>
<td>Width of resonant gate, (μm)</td>
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<td>( d_{\text{eff}}^m )</td>
<td>Magnetostrictive coefficient, (ppm/Oe)</td>
<td>3.4(± 6Oe)</td>
</tr>
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</table>
actual films.

Figure 10 shows the equivalent circuit of MEFGT. The output signal can now be written as,

\[ V_{\text{signal}} = \left( g_m \frac{V_{\text{bias}}}{C_g + C_p + C_0} - r_0 \right) \cdot C_{\text{eff}} \frac{\delta}{d_{\text{eff}}} \]  \hspace{1cm} (8)

where \( g_m \) and \( r_0 \) are the transistor transconductance and output resistance respectively, \( C_g \) is the input gate capacitance, \( C_p \) is the parasitic capacitance, \( C_0 \) is the DC air capacitance and \( V_{\text{bias}} \) is the gate bias voltage. The sensitivity is proportional to the effective air gap capacitance and cantilever deflection.

B. Noise Modeling

The overall noise sources in the MEFGT can be divided into three parts: transistor flicker noise, cantilever thermomechanical noise, and magnetostrictive noise. For the transistor in saturation, the expression for flicker noise spectrum is given by [21]

\[ S_{\text{fg}} = \frac{q^2 N_{\text{ot}}}{C_{\text{ox}}^2 W L} \frac{1}{f} \]  \hspace{1cm} (9)

where \( C_{\text{ox}} \) is oxide capacitance per unit area, \( W \) and \( L \) are transistor width and channel length respectively, and \( N_{\text{ot}} \) is the equivalent oxide trap density. Thermomechanical noise arises as a consequence of the cantilever being in thermal equilibrium with its environment. With the assumption that the thermomechanical noise spectrum is white (i.e. frequency independent), the cantilever deflection noise spectral density can be expressed as [22]

\[ S_x = \frac{4k_B T}{k_{\text{eff}} \omega_0 Q} \]  \hspace{1cm} (10)

where \( k_B \) is the Boltzmann constant, \( T \) is the temperature, \( \omega_0 \) is the resonant frequency, \( Q \) is the quality factor, and \( k_{\text{eff}} \) is effective stiffness constant [23]. By substituting equation (10) into (6) & (8), we can calculate the equivalent cantilever thermomechanical output noise voltage. Magnetostrictive noise also yields a \( 1/f \) type noise which is indicative of a localized low-frequency relaxation in the magnetostrictive response [24]. Figure 11 summarizes the output noise voltages originating from the three main noise sources. The transistor flicker noise, which is limited by the current fabrication process of surface channel transistors, is the dominant noise source in our MEFGT.

Fig. 11. Noise density of MEFGT. including measured transistor flicker noise, modeled cantilever thermal fluctuation and magnetostrictive noise. The transistor flicker noise is dominant over the entire frequency range.

VI. CONCLUSION AND DISCUSSION

In summary, we have successfully designed, fabricated, and characterized an integrated Metglas® magnetoelectric flexural transistor capable of nanoTesla detection and picoTesla magnetic fields under resonance condition. In order to achieve this, we have successfully deposited Metglas® films using ion beam sputtering technique. The magnetic properties of the deposited Metglas® films were characterized through the B-H loop and the magnetostrictive properties were measured as a function of applied magnetic field. The mechanical resonance of the fabricated cantilever structure was measured to be 4.27
Furthermore, by achieving a better stress control in the MS there will be a significant reduction in the transistor noise readout transistors. By introducing more advanced buried noise level in our devices is limited by the charge sensing Tesla minimum detectable field. It should be noted that the University for the Metglas® MEMS device using magnetostrictive cantilever and FET will considerably improve the effective sensitivity as shown in cantilever structures with much smaller effective air gap. This detection at 1 Hz in future work.

also be used in array format to determine not only the magnitude but also the direction of the magnetic field vector. By comparing the results in this work to the other current magnetic sensing technology [1-3, 26], we have benchmarked our MEGFT performance in Figure 12 (a). The MEGFT is comparable to the magnetoresistor and has the potential to challenge the flux gate sensors or even SQUID to achieve pico Tesla minimum detectable field. It should be noted that the noise level in our devices is limited by the charge sensing readout transistors. By introducing more advanced buried channel transistors, such as SiGe quantum well FETs [25], there will be a significant reduction in the transistor noise level resulting in an enhanced integrated sensor performance. Furthermore, by achieving a better stress control in the MS cantilever films, it should be possible to fabricate flat cantilever structures with much smaller effective air gap. This will considerably improve the effective sensitivity as shown in Figure 11(b). These considerations suggest the strong potential of the next generation MEGFT in achieving picoTesla detection at 1 Hz in future work.

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REFERENCES

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Dr. Schiffer is a Fellow of the American Physical Society. He has served as the Chair of the American Physical Society Topical Group on Magnetism and its Applications and as the Program Chair of the 2007 Conference on Magnetism and Magnetic Materials. He will assume the role of Chair of the American Physical Society Division of Materials Physics in 2011. He is the recipient of a Career Award from the National Science Foundation, a Presidential Early Career Award for Scientists and Engineers from the Army Research Office, an Alfred P. Sloan Research Fellowship, and the Faculty Scholar Medal in the Physical Sciences and the Joel and Ruth Spira Award for Teaching Excellence from Penn State.

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