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Processing and Characterization of GaSb/High-k Dielectric Interfaces

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The GaSb surface was exposed to various HCl-based chemical treatments in order to prepare it for high-k dielectric integration. The chemical and structural analysis was performed by X-ray photoelectron spectroscopy (XPS) and atomic force microscopy (AFM). The chemical analysis indicates the HCl effectively removes native oxide from GaSb surface and leaves it slightly antimony rich. The structural study reveals that the surface roughness increases upon the reexposure to air after the HCl-based surface treatment. Additional information was obtained from photo-conductance decay (PCD) measurements as well as C-V measurements of metal-Al₂O₃-GaSb capacitors.

Introduction

Recently, antimonide-based compound semiconductors are being pursued for their potential usefulness in next generations' low-power logic applications (1,2,3). Among them, n-type $InAs_ySb_{1-y}$ quantum-well field effect transistors (QWFETs) in conjunction with p-type $In_xGa_{1-x}Sb$ QWFETs are of interest for low-power complementary logic applications (4,5). A thin layer of GaSb is formed on top of $Al_{0.8}In_{0.2}Sb$ barrier layer to avoid Al interaction with the dielectric and the associated surface oxidation. In such a configuration, GaSb is a material surface properties of which are of critical importance in the process of high-k dielectric integration.

The GaSb surface is chemically very reactive and as such forms a few nanometerthick films of Ga-oxide and Sb-oxide. Upon exposure to air GaSb surface will form a native oxide consisting of Ga_2O_3 and Sb_2O_3 according to the reaction (6,7)

$$2 \operatorname{GaSb} + 3 \operatorname{O}_2 \to \operatorname{Ga}_2 \operatorname{O}_3 + \operatorname{Sb}_2 \operatorname{O}_3$$
^[1]

Subsequently, Sb₂O₃ reacts with GaSb forming additional Ga₂O₃ and elemental Sb which are the only stable phases that can exist in thermodynamic equilibrium.

$$2 \operatorname{GaSb} + \operatorname{Sb}_2 \operatorname{O}_3 \to \operatorname{Ga}_2 \operatorname{O}_3 + \operatorname{Sb}$$
^[2]

This reaction occurs even at room temperature and the resultant elemental Sb can form a thin metallic layer, thus increasing the surface leakage (7). This property of GaSb

significantly hampers the integration of GaSb with high-k gate dielectrics and causes high interface trap density.

It is known that the HCl-based surface treatment effectively removes both Ga-oxide and Sb-oxide from GaSb surface (8). However, the impact of the direct HCl exposure needs to be further explored to evaluate its effect of surface morphology as well as properties of high-k dielectric-GaSb interface. The goal of this study is to investigate the effectiveness of the HCl-based surface treatment in the preparation of oxide-free, flat GaSb surfaces adequate for thin high-k dielectric atomic layer deposition (ALD).

Experimental

In this experiment, Te-doped, (100)-oriented, 2-inch GaSb substrates were used. In the preliminary examination of the effect of HCl-based surface cleaning, two different pre-ALD surface treatments were investigated: Process 1 in which GaSb wafers were only degreased in acetone and isopropanol for 5 min, and Process 2 in which wafers were degreased and then etched in HCl(1):H₂O(2) solution for 10 min then rinsed in isopropanol alcohol. After the surface cleaning, samples were blown with nitrogen and subjected to XPS (X-Ray Photoelectron Spectroscopy) and AFM (Atomic Force Microscopy) analysis. The XPS analysis was carried out using Kratos Axis Ultra XPS with a monochromatic Al K α X-ray source. The binding energy (BE) was calculated using

$$BE = hv - KE - \phi$$
 [3]

where hv is the excitation energy, KE is the measured kinetic energy of the ejected electrons and φ is the work function of the spectrometer.

Next in this experiment the GaSb MOS capacitors with Al_2O_3 as a dielectric were fabricated for electrical characterization of Al_2O_3 -GaSb interfaces. The GaSb substrates were loaded into ALD chamber where 5 nm thick Al_2O_3 was deposited at 300°C by cyclic pulses of trimethylaluminum (TMA) and H_2O (TMA first process). The process of MOS capacitors fabrication was completed by e-beam evaporation of Pt/Au gate contacts and Pd/Au back contacts. Forming gas anneal (FGA) was carried out under forming gas (96% $N_2 + 4\% H_2$) at 350°C for 30 minutes. Additional electrical characterization was carried out on Al_2O_3 -GaSb samples using photo-conductance decay (PCD) method described in details in Ref. (9).

Results and Discussion

Figure 1 shows the XPS results in the Ga $2p_{3/2}$ and Sb $3d_{3/2}$ regions after two different cleaning procedures employed in this study. Samples in Process 1 show the presence of both Ga-oxide and Sb-oxide (Fig. 1a). Oxide coverage is greatly reduced in the case of Process 2 (Fig. 1b) indicating effective oxide removal by HCl(1):H₂O(2) solution. However, the resulting surface is slightly antimony rich. Hence, a potential concern is the increase of the peak representing elemental Sb (Fig. 1b) resulting from the reaction of Sb-

oxide in contact with GaSb wafer as elemental Sb can form a thin metallic layer which will create a surface leakage path (7).



Figure 1. XPS spectra of Ga $2p_{3/2}$ and Sb $3d_{3/2}$ before and after the HCl-based surface treatments. (a) GaSb sample after being degreased (Process 1) and (b) being dipped in HCl for 10 minutes and rinsed by isopropanol alcohol (Process 2).

The surface roughness of samples from two different surface treatments was measured by AFM. The scan area is $1 \times 1 \mu m^2$. The results for as-received GaSb sample after being degreased (Process 1), yielded root-mean-square (rms) surface roughness of ~0.75 nm. The surface roughness increased to ~3.8 nm after samples being dipped in HCl for 10 minutes. Based on the AFM measurements performed in this study it is suggested that the reexposure of the HCl-treated GaSb surface to air causes the irregular growth of oxide islands, thereby, increasing the surface roughness. The possible roughening of the GaSb surface is a critical factor in the preparation of the flat GaSb surface for successful integration with high-k dielectric.

Figure 2 shows the capacitance voltage (C-V) characteristics for GaSb MOS capacitors representing two processes investigated. Figure 2a shows the C-V curves for Process 1 which indicate that the Fermi level is strongly pinned and very fast interface traps alter the response of the MOS capacitor. Figure 2b shows the C-V curves for Process 2 where improved Fermi level modulation is observed. However, it still exhibits the high density of interface trap in both the accumulation and inversion mode. To suppress the effect of interface trap, the sample from Process 2 was subsequently annealed in forming gas $(4/96\%:H_2/N_2)$ at 350°C for 30 minutes and the resulting C-V characteristics are shown in Fig. 2c. After FGA, the interface trap density in the negative bias regime appears to be reduced. On the other hand, however, the maximum capacitance was decreases as well suggesting the increase of the thickness of the interfacial oxide.



Figure 2. C-V characteristics as a function of frequency of n-type GaSb MOS capacitors (a) after the degrease step (Process 1), (b) after HCl-based surface treatment (Process 2) and (c) after HCl-based surface treatment (Process 2) and forming gas anneal at 350°C for 30 minutes.



Figure 3. Extracted interface trap density from n-type GaSb MOS capacitors from Process 2 before and after FGA at 350°C for 30 minutes

Figure 3 shows the interface trap distribution extracted using the conductance method (10) from n-type GaSb MOS capacitors for Process 2 before and after FGA. As seen in the figure, the annealing results in the reduction of the interface trap density in both positive and negative bias regime, although the peak value still remains high (peak $D_{it}=2.9 \times 10^{13} \text{ cm}^{-2} \cdot \text{eV}^{-1}$).

The measurements of the near-surface minority carrier lifetime (τ_{ns}) in GaSb, which was subjected to three different treatments prior to ALD of 5 nm Al₂O₃, were carried out in order to get additional insight into the effect of surface treatments under investigation on the properties of Al₂O₃-GaSb interface. It should be pointed out that the PCD method used here allows measurement of the minority carrier lifetime in the near-surface region of semiconductor (GaSb) covered with a layer of dielectric (Al₂O₃) as long as dielectric is transparent to UV light used for illumination. If this condition is met, as it is in the case of material system of interest in this study, the results obtained are taken as an adequate representation of the condition of the interface region in semiconductor.



Figure 4 Minority carrier lifetime in GaSb subjected to three different surface treatments prior to atomic layer deposition of 5 nm thick Al₂O₃: Processes 1 and 2 are defined in Experimental section.

The τ_{ns} was determined from the slope of the plots in Fig. 4 during the initial period of the photoconductive decay before the carriers generated near the surface diffuse into the bulk As the results in Fig. 4 show, very short minority carrier lifetimes were measured indicating overall low quality of the GaSb region near the interface with Al₂O₃. For comparison, minority carrier lifetimes measured for single-crystal silicon covered with the same 5 nm thick Al₂O₃ were in the range of 20-30 µsec. In spite of the short lifetimes measured the results in Fig. 4 show the difference in photo-conductance decay behavior depending on the processing scheme. The shortest τ_{ns} measured for Process 2 is consistent with increased surface roughness observed following native oxide removal in the course of HCl treatment. The FGA applied after HCl treatment appears to at least partially remove damage introduced by HCl, although, possibility that the FGA affected other properties of the near-surface region can not be excluded (11). Either way this result is consistent with the effect of FGA on the interface trap density (Fig. 3). By allowing the native oxide on the GaSb surface prior to Al₂O₃ deposition, Process 1 results

in longer lifetime as compared to Process 2, but, as concluded from Fig. 2, is not a viable solution from the point of view capacitance-voltage characteristics.

Conclusions

The results obtained in this study confirm that the HCl-based treatment of GaSb surface is effective in the removal of native oxides. Even partial removal of the native oxides considerably improves the C-V characteristics of metal-Al₂O₃-GaSb capacitors. At the same time, however, the same process causes shortening of the minority carrier lifetime near the Al₂O₃-GaSb interface. Further modifications of the GaSb surfaces prior to high-k dielectric deposition can be achieved by forming gas anneal.

Overall, the results obtained in this experiment demonstrate complex interactions between pre-ALD GaSb surface treatments and electrical characteristics of resulting Al_2O_3 -GaSb structures. It is, therefore, important that the process of GaSb surface preparation for high-k dielectric deposition is carefully optimized. The results obtained also seem to point out to the need to integrate pre-deposition surface treatments of GaSb with subsequent atomic layer deposition of high-k dielectric.

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